

APPLICATION DATA SHEET

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Application Information: Title: CIRCUITS AND METHODS FOR REDUCING INTERFERENCE FROM
SWITCHED MODE CIRCUITS
Classification: Class: 455 Subclass:
Tech Center: 2683
Drawing Sheets: 3 Drawing to be Published: 1
Docket No: 1067-CA-CA (P108C1)
Type: UTILITY
Secrecy: None

Representative Information: James J. Murphy, Reg. No. 34,503
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Express Mail Label No:
EL 859 423 482 US

Domestic Priority:

CLAIM TYPE	SERIAL NO.	FILING DATE	STATUS
Continuation of:	09/651,821	August 30, 2000	Pending

CFR §119 Foreign Priority: NONE

CLAIM TYPE	SERIAL NO.	FILING DATE	STATUS

Assignee::

Name: Cirrus Logic, Inc.
Address: 2901 Via Fortuna
Austin, Texas 78746
United States of America

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